

Abstract

An image display apparatus includes scanning circuits for sequentially selecting and scanning row wirings, a modulation circuit for outputting a modulated signal to be applied to column wirings, and a voltage drop compensation means for calculating corrected image data for reducing the influence of voltage drops due to at least resistance components of the row wirings, with respect to image data. The modulated signal is a pulse-width modulated voltage signal having a plurality of voltage amplitude values, and the modulation circuit outputs a modulated signal in which a pulse width and/or a voltage amplitude value of the modulated signal are expanded on the basis of the corrected image data.